

USPTO PATENT FULL-TEXT AND IMAGE DATABASE

[Home](#)
[Quick](#)
[Advanced](#)
[Pat Num](#)
[Help](#)

[Bottom](#)
[View Cart](#)

Searching US Patent Collection...

Results of Search in US Patent Collection db for:
 (AN/"Uniquify" AND AC/"San Jose"): 12 patents.
 Hits 1 through 12 out of 12

Jump To

Refine Search

PAT. NO.	Title
1 9,898,433	Continuous adaptive data capture optimization for interface circuits
2 9,805,784	Multiple gating modes and half-frequency dynamic calibration for DDR memory controllers
3 9,584,309	Circuit for dynamically adaptive bit-leveling by incremental sampling, jitter detection, and exception handling
4 9,552,853	Methods for calibrating a read data path for a memory interface
5 9,431,091	Multiple gating modes and half-frequency dynamic calibration for DDR memory controllers
6 9,425,778	Continuous adaptive data capture optimization for interface circuits
7 9,300,443	Methods for dynamically adaptive bit-leveling by incremental sampling, jitter detection, and exception handling
8 9,100,027	Data interface circuit for capturing received data bits including continuous calibration
9 9,081,516	Application memory preservation for dynamic calibration of memory interfaces
10 9,075,543	Method of application memory preservation for dynamic calibration of memory interfaces
11 9,021,293	Methods for operating a memory interface circuit including calibration for CAS latency compensation in a plurality of byte lanes
12 8,661,285	Dynamically calibrated DDR memory controller

[Top](#)
[View Cart](#)

[Home](#)
[Quick](#)
[Advanced](#)
[Pat Num](#)
[Help](#)