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AN/"Sierra Circuits": 9 patents.

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PAT. NO.

Title

- 1 [10,306,756](#)  [Circuit board with catalytic adhesive](#)
 - 2 [9,942,981](#)  [Circuit board apparatus and method](#)
 - 3 [9,922,951](#)  [Integrated circuit wafer integration with catalytic laminate or adhesive](#)
 - 4 [9,706,667](#)  [Via in a printed circuit board](#)
 - 5 [9,706,650](#)  [Catalytic laminate apparatus and method](#)
 - 6 [9,674,967](#)  [Via in a printed circuit board](#)
 - 7 [9,631,279](#)  [Methods for forming embedded traces](#)
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